

## NATIONAL INSTITUTE OF TECHNOLOGY PATNA

(An Institute under Ministry of HRD, Govt. of India)

# ASHOK RAJPATH, PATNA-800 005 (BIHAR)

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### Recruitment of Guest Faculty on Contract Basis under SMDP-C2SD Project

Walk-in-Interview for the following post (on Contract Basis) under the project "Special Manpower Development Program for Chips to System Design (SMDP-C2SD)" sponsored by MeitY (Govt. of India), New Delhi will be held on 11th February 2020 at 10:00 AM in the Department of ECE, NIT Patna.

### **Eligibility Criterion:**

Sl.	Post	Qualification	No. of. Posts /	Consolidated Pay
No			Duration	(Per Month)
1	Guest	M.Tech in VLSI / VLSI & Embedded	1 Post /	Rs. 40,000 /-
	Faculty	Systems or its equivalent with first division	1Year OR up to	
		and B.Tech in Electronics and	the completion	
		Communication Engineering with first	of the project	
		division. Candidates having sound	whichever is	
		knowledge on FPGA implementation will	earlier	
		be given preference.		

The appointment will be done on a contract basis for an initial period of one year and is likely to be extended based on performance. Interested candidates should carry the original certificates, a copy of resume mentioning educational qualifications, DOB, professional experience (if any), two references, contact details including email and phone numbers, etc. and a photocopy set of all documents.

An advance copy of resume (by email) may be forwarded to the Principal Investigator at the address given below.

#### Note:

- 1. An applicant has to ensure the authenticity of the information provided in support of experience claimed, other documents and photograph (ID proof).
- 2. No TA/DA will be paid for appearing in the Interview.
- 3. No-Objection certificate has to be submitted by the candidates who are already working.

4. List of selected candidate will be displayed on the Institute website. G. franco 07-01-2020

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Co-Principal Investigator

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**Gayadhar Pradhan** 

Principal Investigator

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