Joint Online Faculty Development Programme
Embedded UVM open source Emulation & Functional Verification

Organized By
E & ICT Academy, NIT Patna

July 13 to July 24 , 2020
Click here to register

Supported By:

Ministry of Electronics and Information Technology, MeitY, Govt. of India.

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About NIT Patna:

National Institute of Technology Patna is the 18th National Institute of Technology created by the Ministry of H.R.D. Government of India after rechristening the erst while Bihar College of Engineering Patna on 28/01/2004. The Institute imparts high level education training, research and development in science, engineering technology and humanities alongwith high quality education and values at UG, PG and Ph.D. level.
At present the Institute offers courses in six major technical disciplines viz. Architecture, Civil Engineering, Computer Science & Engg., Electrical Engg., Electronics & Communication Engg. and Mechanical Engg. It also consists of well-established departments of Physics, Chemistry, Mathematics and Humanities and Social Sciences.

About E&ICT Academy NIT Patna:

Ministry of Electronics and Information Technology, Government of India has instituted seven Electronics and Information & Communications Technology (ICT) Academies of which, the academy of NIT Patna is one. The Academy at NIT Patna aims to design and organize basic as well as specialized training programmes in niche areas of electronics and ICT for the development required knowledge base, skills and tools to equip the teaching community with better knowledge and understanding.

Speakers :

(i) Inaugural Speaker :
- Ms. Dora Smith
  Academic program(USA), Siemens

(ii) Industry Expert :
- Mr. Puneet Goel
- Mr. Dinesh Gupta

(iii) Other Speakers-
- Mr. Devesh Dwivedi
  (Samsung Research Lab)
- Mr. Anand Venkitachalam
  (Western Digital)
- Mr. Ruchir Dixit
  Managing Director (Siemens-Mentor)
- Mr. Israr Ahamed Sheikh (Intel),
- Mr. Gaurav Jalan, Founder & CEO of
- SpicaWorks (confirmation awaited)
- Dr. Virendra Singh, IITB (Awaited)
- Dr. Gaurav Trivedi, IITG

Course Contents :

→ Introduction to Discrete Event Simulation Technology, Verilog, and Functional Verification, Getting acquainted with Simulation Tools.

→ Data Communication in Hardware, Bus functional models, Types of Hardware Buses (Serial, Memory Mapped, Streaming), UVM report mechanism.

→ Concepts of object-oriented programming, EUVM data types and program structure.

→ UVM Phasing and Objection mechanisms, TLM ,OOP Design Patterns (Template and Strategy)

→ Advanced UVM Concepts – Factory, Callbacks – OOP Design Patterns (Factory and Observer), Concepts of SoC Verification.

Course Fee

Certification Fee:-
Faculty/ PhD Scholar = **Rs.500/-**
(SC/ST = **Rs. 250/-**)
Others (Except Faculty/PhD-Scholar): **1000/-**
(SC/ST = **Rs. 500/-**)

Online payment details :-
Bank Name: Allahabad Bank
Account Name: NIT Patna
Account No.: **50380476798**
IFSC Code: **ALLA0212286**

Link for registration :

https://forms.gle/J3SM9eaTTesnQkxh6