



<h3>Objective & Scope</h3> <p>To enable theoretical and practical understanding RISC-V processor designing for circuits and architecture.</p>	<h3>Coordinators</h3> <p>Dr. Sangeeta Singh, NIT Patna Sangeeta.singh@nitp.ac.in (Ph.- +91 9479646111) Dr. Pankaj Kumar, NIT Patna pankajjha@nitp.ac.in (Ph.- +91 7004727085)</p>
<h3>Speakers</h3> <ul style="list-style-type: none">▪ Puneet Goel (VLSI Expert)▪ Dr. Aditya Dalakoti (VLSI Expert)▪ Puneet Mittal (VLSI Expert)	<h3>Course Contents</h3> <ul style="list-style-type: none">▪ Flow Overview▪ Transistor Level and Gate Level Design and Simulation▪ Digital Logic Design▪ Data path & control pipelining▪ RISC-V ISA▪ RISC-V ISA Simulation▪ Processor from Ground Up▪ Synthesis▪ VLSI Backend Flow▪ Processor Sign Off▪ Power Performance Area Trade Off▪ RISC-V Industry and Jobs
<h3>Key Features</h3> <ul style="list-style-type: none">▪ Instructor led online training sessions▪ 7-8 hours per day▪ Rigorous Hand on (practice)▪ Sessions on Circuit Simulations using CppSim▪ ISA Simulations using Spike ISA▪ Processor from ground up using▪ GNU Tool Chain▪ Synthesis using Yosys	<h3>Course Fee Details</h3> <p>Academic (student/faculty): 500 INR Industry People/Others : 1000 INR Foreign Participants: 4000 INR</p> <p>Online payment details</p> <p>Bank Name: Indian Bank/Allahabad Bank Account Name: NIT Patna Account No.: 50380476798 IFSC Code: IDIB000B810</p>
<h3>Link for registration:</h3> <p>https://forms.gle/gTXWtLa25dutu5sv6</p>	<p>Last Date for registration : February 03, 2022</p>

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For more details visit:

<http://www.nitp.ac.in/ict/index.php>

Email: eictapatna@nitp.ac.in

Contact No.: 7091406964, 9479646111, 7004727085