

Master of Civil Engineering (ECPG) Microelectronics & VLSI-DD

Sem.	Paper	Paper Name	Type	Full Mark	L	T	P	Credit	Compulsory/ Elective
1	1EC661	MOS Device Physics and Modelling	T	100	3	0	0	3	Compulsory
1	1EC662	Analysis and Design of digital integrated circuits	T	100	3	0	0	3	Compulsory
1	1EC6XX	Elective-I	T	100	3	0	0	3	Elective
1	1EC6XX	Elective-II	T	100	3	0	0	3	Elective
1	1EC6XX	Elective-III	T	100	3	0	0	3	Elective
1	1EC663	Digital Design lab	P	100	0	0	6	4	Compulsory
2	2EC664	Analysis and Design of Analog Integrated Circuits	T	100	3	0	0	3	Compulsory
2	2EC6XX	Elective-IV	T	100	3	0	0	3	Elective
2	2EC6XX	Elective-V	T	100	3	0	0	3	Elective
2	2EC6XX	Elective-VI	T	100	3	0	0	3	Elective
2	2EC6XX	Elective-VII	T	100	3	0	0	3	Elective
2	2EC665	Analog & Mixed Signal Lab	P	100	0	0	6	4	Compulsory
2	2EC691	Seminar	P	100	0	0	3	2	Compulsory
3	3EC692	Seminar & Comprehensive Viva	P	100	0	0	3	2	Compulsory
3	ME701	Research Methodology	T	100	3	0	0		Compulsory
3	3EC693	Dissertation	P	100	0	0	16	8	Compulsory
4	4EC694	Comprehensive Vi va	P	100	0	0	3	2	Compulsory
4	4EC695	Dissertation	P	100	0	0	20	10	Compulsory

List of Electives:

1	EC671	VLSI and MEMS Technology	T	100	3	0	0	3	Elective (PG VLSI)
2	EC672	Semiconductor Materials, Devices & Characterization	T	100	3	0	0	3	Elective (PG VLSI)
3	EC673	Low Power VLSI Design	T	100	3	0	0	3	Elective (PG VLSI)
4	EC675	Clean room technology and Maintainance	T	100	3	0	0	3	Elective (PG VLSI)
5	EC676	ULSI Technology	T	100	3	0	0	3	Elective (PG VLSI)
6	EC677	VLSI Physical Design	T	100	3	0	0	3	Elective (PG VLSI)
7	EC678	VSLI Mixed Signal Design	T	100	3	0	0	3	Elective (PG VLSI)

8	EC679	DSP in VLSI	T	100	3	0	0	3	Elective (PG VLSI)
9	EC680	CAD for VLSI	T	100	3	0	0	3	Elective (PG VLSI)
10	EC681	Testing and Testability	T	100	3	0	0	3	Elective (PG VLSI)
11	EC682	VLSI DSP Architecture	T	100	3	0	0	3	Elective (PG VLSI)
12	EC683	Hardware/Software Co- Design	T	100	3	0	0	3	Elective (PG VLSI)
13	EC684	Hardware Description Languages	T	100	3	0	0	3	Elective (PG VLSI)
14	EC685	FPGA Design	T	100	3	0	0	3	Elective (PG VLSI)
15	EC686	Full Custom Design	T	100	3	0	0	3	Elective (PG VLSI)
16	EC687	ASIC System Design	T	100	3	0	0	3	Elective (PG VLSI)
17	EC689	Formal Verification	T	100	3	0	0	3	Elective (PG VLSI)
18	EC688	GaAs Technology	T	100	3	0	0	3	Elective (PG VLSI)
19	EC690	RF IC Design	T	100	3	0	0	3	Elective (PG VLSI)
20	EC696	MEMS & Microsystems	T	100	3	0	0	3	Elective (PG VLSI)
21	EC697	Organic Electronic	T	100	3	0	0	3	Elective (PG VLSI)
22	EC698	Physical Design Automation	T	100	3	0	0	3	Elective (PG VLSI)
23	EC699	Embedded System Design	T	100	3	0	0	3	Elective (PG VLSI)
24	EC700	Nanoscale Devices	T	100	3	0	0	3	Elective (PG VLSI)
25	EC701	Advanced VLSI Interconnects	T	100	3	0	0	3	Elective (PG VLSI)
26	EC702	Semiconductor Devices Modeling	T	100	3	0	0	3	Elective (PG VLSI)
27	EC703	MEMS and NEMS Technology	T	100	3	0	0	3	Elective (PG VLSI)
28	EC704	VLSI Architecture	T	100	3	0	0	3	Elective (PG VLSI)